

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Judith Maget et al.
Serial No.: 10/574,151
(Priority Application No. DE 103 45 195.1)
(International Application No. PCT/DE2004/002023)
Filed: March 29, 2006
(Priority Date: 29 September 2003)
(International Filing Date: 10 September 2004)
Docket No.: I432.132.101/P32281
Title: INJECTION-LOCKED OSCILLATOR CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Information Disclosure Statement be entered and the documents listed on attached form 1449 be considered by the Examiner and made of record. Any required copies of patents, publications or other documents are enclosed for the Examiner's review. Pursuant to the provisions of M.P.E.P. 609, Applicant further requests a copy of the 1449 form, marked as being considered and initialed by the Examiner, be returned with the next Official Communication.

Since this Information Disclosure Statement is being submitted within three months of filing national application; or date of entry of national application; or before the mailing date of the first Office Action on the merits, a fee has not been enclosed. However, if such fee is required, the Patent Office is hereby authorized to charge Deposit Account No. 500471 for fees as set forth under 37 C.F.R. 1.17(p).

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /JC/

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Applicant respectfully requests consideration of these references during prosecution of the above-identified matter. The Examiner is invited to contact the Applicant's representative at the below-listed telephone number if there are any questions regarding this Communication or the tendered references.

Respectfully submitted,

Judith Maget et al.,

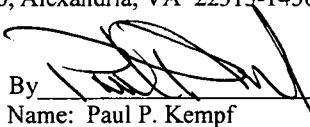
By their attorneys,

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Dated: 04/05/07
PPK:mlm


Paul P. Kempf
Reg. No. 39,727

CERTIFICATE UNDER 37 C.F.R. 1.8: The undersigned hereby certifies that this paper or papers, as described herein, are being deposited in the United States Postal Service, as first class mail, in an envelope address to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 5th day of April, 2007.


By
Name: Paul P. Kempf

FORM PTO-1449		Docket No.: I432.132.101	Serial No.: 10/574,151 Priority Appln. No. DE 103 45 195.1 Int'l Appln. No. PCT/DE2004/002023
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT		Applicant: Judith Magetet et al.	
		Filing Date: March 29, 2006 Priority Date Sept 29, 2003 Int'l Filing Date Sept 10, 2004	Group Art:

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date If Appropriate
/JC/	6,175,285 B1	01/16/2001	Gabara			
/JC/	6,198,360 B1	03/06/2001	Henrion			
/JC/	6,417,740 B1	07/09/2002	Anh et al.			
/JC/	6,617,936 B2	09/09/2003	Dally et al.			

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub Class	Translated Yes No
	NONE					

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

/JC/	"The Design of Low Noise Oscillators"; A. Hajimiri, T.H. Lee; Kluwer, Boston (1999); ISBN 0-7923-8455-5
/JC/	"A 900-MHz CMOS LC Oscillator with Quadrature Outputs"; A. Rofougaran et al.; IEEE Int. Solid-State Circuits Conf. (ISSCC); Dig. Tech. Papers (Feb. 1996); pp. 392-393
/JC/	"Analysis and Design of a 1.8 GHz CMOS LC Quadrature VCO"; P. Andreani et al.; Journal of Solid State Circuits vol. 37 No. 12 (Dec. 2002) pp. 1737-1747
/JC/	"A 1.8 GHz CMOS Quadrature Voltage Controlled Oscillator (VCO) Using the Constant Current LC Ring Oscillator Structure"; C.J. Wu, H.S. Kao; International Symposium on Circuits and Systems (1998) pp. 378-381
/JC/	"Low Power, Low-Phase-Noise differentially tuned Quadrature VCO Design in Standard CMOS"; Tiebout M., Journal of Solid State Circuits, Vol. 36, No. 7, (Jul. 2001) pp. 1018-1024
/JC/	"Single-Chip 900 MHz Spread-Spectrum Wireless Transceiver in 1- μ m CMOS-Part I: Architecture and Transmitter Design", A. Rofougaran et al.; Journal of Solid State Circuits, Vol. 33, No. 4, (Apr. 1998) pp.515-534
/JC/	"A Low-Phase-Noise CMOS LC Oscillator with a Ring structure", J.J. Kim and B. Kim; International Solid State Circuits Conference, Vol. XLIII (Feb. 2000) p. 430

EXAMINER: /Jason Crawford/ (05/06/2009)

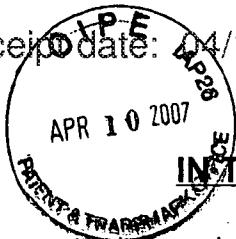
DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /JC/

Receipt Date: 04/10/2007

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3634 PJP



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Examiner: Unknown

Serial No.: 10/574,151

Group Art Unit:

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Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

We are transmitting herewith the attached:

Transmittal Sheet containing Certificate of Mailing under 37 C.F.R. 1.8

Information Disclosure Statement 2 pg(s).

Form PTO-1449 1 pg(s).

Copy of 7 cited references

The Commissioner is authorized to charge \$0.00 to Deposit Account No. 500471 to cover the required fee.

Other: _____

Return Postcard

Customer No. 025281

By

Name: Paul P. Kempf (Reg. No. 39,727)

Please consider this a Petition for Extension of Time for a sufficient number of months to enter these papers, if appropriate. At any time during the pendency of this application, please change any additional fees or credit overpayment to Deposit Account No. 500471.

CERTIFICATE UNDER 37 C.F.R. 1.8:

Date of Deposit: 04/05/07

The undersigned hereby certifies that this paper or papers, as described herein, are being deposited in the United States Postal Service as First Class Mail, addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Name: Paul P. Kempf

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /JCP